TOSHIBA

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE

# 2SK1739A

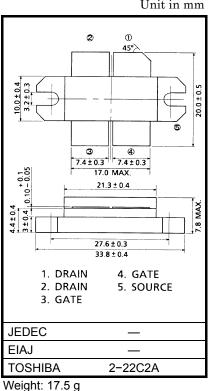
#### RF POWER MOS FET for UHF TV BROADCAST TRANSMITTER

- Output Power  $: Po \ge 90 W (Min.)$ •
- Drain Efficiency
- $: \eta_{\rm D} = 50\%$  (Typ.)
- : f = 770 MHz Frequency
- Push–Pull Structure Package

#### ABSOLUTE MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	80	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current	۱ <sub>D</sub>	11	А
Reverse Drain Current	I <sub>DR</sub>	11	А
Drain Power Dissipation	PD	250	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature Range	T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high



temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Unit in mm

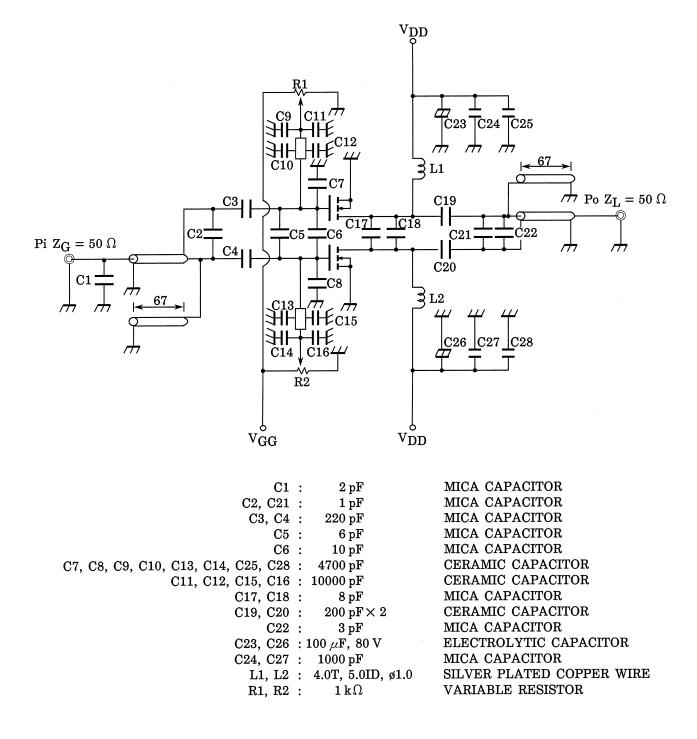
### ELECTRICAL CHARACTERISTICS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Power	Po	V <sub>DD</sub> = 40 V, lidle = 0.2 A × 2	90	110	_	W
Drain Efficiency	ηD	Pi = 10 W, f = 770 MHz *	_	50	_	%
Drain-Source Breakdown Voltage	V (BR) DSS	I <sub>D</sub> = 5 mA, V <sub>GS</sub> = 0	80	_	_	V
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0	_	_	1.0	mA
Gate Threshold Voltage	V <sub>th</sub>	I <sub>D</sub> = 0.5 mA, V <sub>DS</sub> = 10 V	0.5	_	3.0	V
Drain-Source ON Resistance	R <sub>DS (on)</sub>	I <sub>D</sub> = 2 A, V <sub>GS</sub> = 10 V **	_	0.5	1.5	Ω
Drain-Source ON Voltage	V <sub>DS (on)</sub>	I <sub>D</sub> = 2 A, V <sub>GS</sub> = 10 V **	_	1.0	3.0	V
Forward Transfer Admittance	Y <sub>fs</sub>	I <sub>D</sub> = 1.5 A, V <sub>DS</sub> = 20 V **	0.9	1.3	_	S
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0, f = 1 MHz	_	80	_	pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0, f = 1 MHz	_	40	_	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0, f = 1 MHz	_	1	_	pF

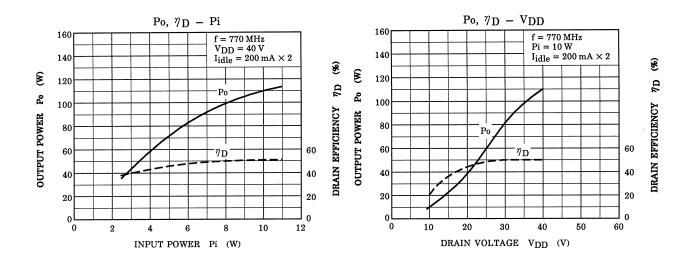
\*: Push-Pull Operation \*\*: Pulse Test

This transistor is the electrostatic sensitive device. Please handle with caution.

#### **RF OUTPUT POWER TEST FIXTURE**



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#### CAUTION

These are only typical curves and devices are not necessarily guaranteed at these curves.

#### **RESTRICTIONS ON PRODUCT USE**

20070701-EN GENERAL

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- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
   In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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